CURRENT MODE FUZZY BASED CONTROLLER FOR MULTILEVEL INVERTER

MOHAMAD ANIM BIN MISIRAN

A project report submitted in partial fulfilment of the requirements for award of degree of Master of Engineering (Electrical-Power)

Faculty of Electrical Engineering
Universiti Teknologi Malaysia

JANUARY 2013
Dedicated to my beloved father & mother

Misiran Bin Mastary & Siti Missiah Binti Ramtimin

My Beloved Wife

Fadila Binti Lazim

My Beloved Children

Ridzwan Bin Mohamad Anim

Adelia Binti Mohamad Anim

And

My Entire friend in MEP programme

For their encouragement
ACKNOWLEDGEMENT

BISMILLAHIRAHMANIROHIM

In the name of Allah, the Most Beneficent and the Most Merciful. It is deepest sense gratitude of the Almighty that gives me strength and ability to complete this final project report.

First of all I would like to express my thanks to my supervisor, Dr. Shahrin Md Ayob for his guidance, support and also motivation throughout completing this master’s project.

I also would like to thanks to my dearest parents fiancée and siblings for their spiritual encouragement, continuous belief, encouragement, motivation and moral support.

Last but not least, my appreciation goes to my fellow friends for sharing their ideas, advises and dedicated assistance during the process of completing the study.
ABSTRACT

The thesis presents the control design for H-bridge cascaded multilevel inverter using current mode fuzzy based controller. The cascaded multilevel inverter synthesizes a five level output voltage from a several independent source. Using multilevel technique, the amplitude of the voltage is increase, stress in the switching device is reduce and the overall harmonics profile is improve. Fuzzy logic is utilized to compensate any changes at the output due to disturbance, system parameters changes, nonlinearities and etc. The full inverter system, is designed and simulated using Matlab-Simulink. From the simulation results, it was shown that the controller is capable to compensate disturbances hence verify the design.
ABSTRAK

# TABLE OF CONTENTS

<table>
<thead>
<tr>
<th>CHAPTER</th>
<th>TITLE</th>
<th>PAGE</th>
</tr>
</thead>
<tbody>
<tr>
<td>TITLE</td>
<td></td>
<td>i</td>
</tr>
<tr>
<td>DECLARATION</td>
<td></td>
<td>ii</td>
</tr>
<tr>
<td>DEDICATION</td>
<td></td>
<td>iii</td>
</tr>
<tr>
<td>ACKNOWLEDGEMENT</td>
<td></td>
<td>iv</td>
</tr>
<tr>
<td>ABSTRACT</td>
<td></td>
<td>v</td>
</tr>
<tr>
<td>ABSTRAK</td>
<td></td>
<td>vi</td>
</tr>
<tr>
<td>TABLE OF CONTENT</td>
<td></td>
<td>vii</td>
</tr>
<tr>
<td>LIST OF TABLES</td>
<td></td>
<td>ix</td>
</tr>
<tr>
<td>LIST OF FIGURES</td>
<td></td>
<td>x</td>
</tr>
<tr>
<td>LIST OF ABBREVIATION</td>
<td></td>
<td>xii</td>
</tr>
</tbody>
</table>

## 1 INTRODUCTION

1.1 Introduction 1
1.2 Problem Background 2
1.3 Problem Statement 3
1.4 Objective 3
1.5 Project Scope 4
1.6 Significance of Project 5
1.7 Organization of Report 5
2 LITERATURE REVIEW

2.1 Introduction 6
2.2 Basic Inverter Concept 7
2.3 Multilevel Inverter 10
2.4 Topologies of Multilevel Inverter 12
   2.4.1 Diode-Clamped Multilevel Inverter (DCMI) 12
   2.4.2 Flying Capacitor Multilevel Inverter (FCMI) 16
   2.4.3 Modular Structured (cascaded) Multilevel Inverter (MSMI) 18
2.5 Comparison Among Three Multilevel Inverters in Renewable Energy Sources 22
2.6 The Carrier-Based Pulse width Modulation (PWM) Technique 23
2.7 Modulation Methods for Cascaded Multilevel Inverter 25
2.8 Fuzzy Logic Control 28
2.9 Project Difficulties and Problem Statement 31
   2.9.1 Summary 33

3 METHODOLOGY

3.1 Introduction 35
3.2 Simulation of Five-level MSMI 36
   3.2.1 Layout partition 37
   3.2.2 Multilevel Inverter Circuit 38
   3.2.3 PWM controller 39
3.3 Comparing PWM Control Circuit with Theoretically 40
3.4 Fuzzy Logic control 44
   3.4.1 Typical Fuzzy Logic Design Procedure 45
4 SIMULATION AND RESULT

4.1 Simulation PWM Switching Method 49

4.2 Simulation MSMI Output 53

4.2 Simulation MSMI Output Effect on Changing Load 54

5 CONCLUSION AND FUTURE WORK

5.1 Summary of Work 57

5.2 Contribution of the Study 58

5.3 Suggestion for Future works 59

REFERENCES 60
LIST OF ABBREVIATION

THD  Total Harmonic Distortion

PWM  -  Pulse Width Modulation
CHAPTER 1

INTRODUCTION

1.1 Introduction

Nowadays many applications such as uninterruptible power supply (UPS), adjustable speed AC motor drive and AC home appliance are use inverter type. Its convert direct current (dc) to alternate current (AC). The purpose of inverter circuit is to convert an AC voltage from the source of DC voltage normally comes from battery or other DC source. This project will concentrate to produce AC output from the DC input with close loop by using current mode fuzzy logic as base controller to control inverter. The main impetus for this is the fact that electrical equipment and machineries have begun to demand for power converters in the range of several Megawatts to be connected to a medium voltage network (2.3-6.9kV) [1]. For the large power conversion systems, one approach inverter to solve which to use for high power connected to the grid, the multilevel inverter to be proposed. The multilevel inverter can improve harmonics performance which eliminate for switching losses reduction. In contrast, where the proportional to the number of output voltage level, the stress on each switch can be reduce.


1.2 **Problem Background**

An inverter change a DC or battery power into AC or household power through waves which called either sine waves or modified waves. Single stage inverter produced low voltage output and harmonics contain. Recently, the multilevel inverters have received more attention in literature due to their ability to synthesize waveforms with a better harmonic spectrum and to attain higher voltages[2]. The output voltage waveform of a multilevel inverter is composed of a number of levels of voltages starting form three levels and reaching infinity depending upon the number of the dc sources[3]. System need in close loop operation to remains stable output even any external disturbance occur. In open loop, the stability of the appliances will effect due to efficiency needed. Compared with open loop control system, the close loop operation control has the advantage of gaining precise inverter output voltage.

The study is expected to find way of reducing harmonics which effected from switching components and produce higher voltage for supply to the load. The fuzzy logic approach has been objected of an increasing interest and has found application in many domains of control problem. The main advantages of fuzzy logic control method as compared to conventional control techniques resides on the fact that no mathematical model is required for controller design and also it does not suffer much from the stability problem but it need the experts experience[4]. The considerations from the simulation result will be explained throughout the report.
1.3 Problem Statement

The topology of single phase H-bridge inverter and multilevel inverter has been defined. In this project, the multilevel inverter has been constructed based on single phase single inverter from the basic H-bridge fundamental. The output voltage waveform from the open loop and close loop has been analysed. The switching method by using pulse width modulation (PWM) using ideal switch from the MATLAB simulink at certain injected frequency have been observed. The low pass passive filter from desired value will be calculated and adjusted to produce better waveform output similar which AC needed. Incorrect chosen filter will effect to the output waveform voltage.

The idea from the sine wave output implemented to the multilevel inverter with additional expandable from the control switch. At the beginning stage, the project implemented to multilevel inverter facing due to get better sinusoidal voltage output. Mostly the selection low pass passive filter effect the waveform output and the control unit which combination a few of logic gate and signal generator is important thing need consideration. Finally stage all the value and parameters with close loop operation with combination of fuzzy logic based on current mode has been successfully define and simulation result will be explained throughout the report.

1.4 Objective

Few objectives have been identified in this study:

a) To proposed a current mode fuzzy based controller for cascaded in multilevel inverter.
b) To develop stable AC output and clean which provide by monitoring by fuzzy logic regardless of the load type connected to it.

c) To develop relevance multilevel inverter simulate using MATLAB simulink software.

d) To compare the performance of these methods.

1.5 Project Scope

This study will reflect the estimation of close loop current mode with fuzzy logic controller for output sinusoidal output waveform. The scope of this study as shown below:

a) The method concentrates on the design H-bridge cascade multilevel inverter with minimum two stage and five output level voltage. The cascaded multilevel inverter synthesizes a desired from a several independent source of DC voltage obtained from batteries.

b) Each single DC source is associated with a single H-bridge inverter then the AC terminal voltage of a different level inverter are connected in series. Using multilevel technique, the amplitude of the voltage is increased, stress in the switching device is reduced and the overall harmonic profile is improved.

c) Simulation based on MATLAB simulink will be conducted and the methodology of inverter employs fuzzy logic control has two control input, which are error(e) and change of error(è).
1.6 Significance of Project

There are increasing worldwide home appliance, heavy industries, automotive and power system network used inverter concept such as multilevel inverter. A past few years the inverter have been increasing on demand for high power application. The small important feature if inverter can only produce a small total harmonic distortion. Cascade multilevel inverter proposed in this project with control circuit current mode fuzzy logic to control output voltage using sinusoidal pulse width modulation. Mostly PWM is useful for many electronics circuit due to comparing fundamental voltage and carrier voltage in certain frequency was not complex circuit. The importance of safety, reliability economically wise of inverter module in market increasing the demand. Beside accuracy, the effectiveness of fuzzy logic based current mode controller able to detect voltage and current at sudden fluctuate due to load change.

1.7 Organization of Report

The report consists of five chapters. Chapter 1 presents the introduction of the study, problems statement, objectives and project scope. Chapter 2 gives literature reviews on the methods. The introduction of controller and methodology of entire work is discussed Chapter 3 and Chapter 4 discussed the simulation and the result. The conclusion and suggestions for future work are explained in Chapter 5.
REFERENCES

1) Derivation of Switching Angles of the Cascaded Multilevel Voltage Source Inverter Subjected to a New Pulse Width Modulation Scheme, Zainal Salam and Junaidi Aziz, Faculty of Electrical Engineering, Universiti Teknologi Malaysia.


4) Control of Cascade Multilevel Inverter Using Fuzzy Logic Technique Dr. Rabee’ H. Thejel* and Shafaa M. Salih***, ** College of Engineering, University of Basrah


9) Control of A Single Phase Inverter Using Fuzzy Logic S. M. Ayob, Z. Salam, N. A. Azli Faculty of Electrical Engineering Universiti Teknologi Malaysia 81310 UTM Skudai, Johor, Malaysia Malik E. Elbuluk Department of Electrical Engineering College of Engineering University of Akron, Akron, OH 44325-3904, USA

