SUPPORT VECTOR MACHINE HARDWARE ACCELERATOR FOR TONGUE COLOUR DIAGNOSIS

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DEDICATION

This project report is dedicated to my parents, who taught me never to give up and always strive to be the best. It is also dedicated to my siblings, who provided me with moral support throughout the entire length of the project.

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ABSTRACT

Tongue body features such as colour is used in Traditional Chinese Medicine (TMC) practices to diagnose a patient's state of health. However, the diagnosis of one patient's health condition varies between practitioners. This gives rise to the need of a standard method such as using Support Vector Machine to identify the tongue body colour. SVM is a supervised machine learning algorithm that aims to explore the characteristics and correlations between the gathered data to deduce the most efficient way in classifying the data into groups. Typically, SVM classifiers are implemented in software where the classification performance is very dependent on the architecture of the general-purpose CPU. Since classification of tongue images is a recurring event, the design of a hardware accelerator is explored in this project. The purpose of design a hardware accelerator is to boost the classifier performance, execution time and latency so that it meets real-time constraints. Architectural optimization methods, such as loop unrolling, memory array partitioning, pipelining and adder tree implementation of the SVM classification algorithm are performed in Xilinx's Vivado HLS and later synthesized to target for FPGA implementation. To further optimize the resource utilization, 18-bits IEEE-754 floating-point representations for the floating point units are used. The SVM hardware is able to demonstrate 140x speed up with similar classification accuracy when compared to the software implementation in MATLAB.

ABSTRAK

Ciri-ciri tubuh lidah seperti warna digunakan dalam amalan Perubatan Tradisional Cina (TMC) untuk mendiagnosis keadaan kesihatan pesakit. Bagaimanapun, diagnosis keadaan kesihatan seorang pesakit berbeza antara pengamal. Ini menimbulkan keperluan kaedah standard seperti menggunakan Mesin Vektor Sokongan untuk mengenal pasti warna badan lidah. SVM adalah algoritma pembelajaran mesin yang diselia yang bertujuan untuk meneroka ciri-ciri dan korelasi antara data yang dikumpul untuk menyimpulkan cara yang paling berkesan dalam mengklasifikasikan data ke dalam kumpulan. Biasanya, pengelas SVM dilaksanakan dalam perisian di mana prestasi klasifikasi sangat bergantung pada seni bina CPU tujuan umum. Oleh kerana pengkelasan imej lidah adalah peristiwa yang berulang, reka bentuk pepatah perkakasan diterokai dalam projek ini. Tujuan merancang pemecut perkakasan adalah untuk meningkatkan prestasi pengelas, masa pelaksanaan dan kependaman supaya ia memenuhi kekangan masa nyata. Kaedah pengoptimuman seni bina, seperti gelung pembongkaran, pembahagian array memori, pipelining dan penambah pohon, algoritma klasifikasi SVM dilakukan di Xilinx's Vivado HLS dan kemudian disintesis untuk menargetkan pelaksanaan FPGA. Untuk mengoptimumkan penggunaan sumber, 18-bit perwakilan titik terapung IEEE-754 untuk unit terapung digunakan. Perkakasan SVM mampu menunjukkan kelajuan 140x dengan ketepatan klasifikasi yang sama apabila dibandingkan dengan pelaksanaan perisian di MATLAB.

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LIST OF ABBREVIATIONS

CSD	-	Canonic Signed Digit
CPU	-	Central Processing Unit
CSE	-	Common Subexpression Elimination
FPGA	-	Field Programmable Gate Array
FSM	-	Finite State Machine
GPP	-	General Purpose Processor
GPU	-	Graphics Processing Unit
HDL	-	Hardware Description Language
HLS	-	High Level Simulation
HPC	-	High Performance Computing
LUT	-	Look Up Table
RBF	-	Gaussian Radial Basis Function
SMO	-	Sequential Minimal Optimization
SVM	-	Support Vector Machine
TIAS	-	Tongue Image Analysis System
TMC	-	Traditional Chinese Medicine

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CHAPTER 1

INTRODUCTION

1.1 Background of the Problem

The diagnosis of Traditional Chinese Medicine (TCM) is based on the information obtained in four diagnostic processes, namely, looking, listening, smelling, asking and touching. The most common task is to measure the pulse and examine the tongue. Usually, oriental medicine practitioners' uses features extracted from tongue tests, for example the colour of the tongue, to diagnose a patient's health [1]. Therefore, accurate recognition of tongue colour is crucial for predicting the patient's condition, as it provides useful information about blood coagulation, water imbalance and psychological problems [2].

There are three main groups of tongue colours: light red, red and dark red. The tongue body colour range is very narrow, as it compromises of different types of the identical colour. Colours are hard to distinguish with the naked eye, and there are many overlapping and similar pixels [2]. Traditionally, doctors diagnose diseases and recognize patterns by examining, describing, and experiencing them. As a result, the results are easily influenced by the doctor's expertise and the surrounding environment. Without objective evaluation criteria, the accuracy of pattern recognition and the repeat ability of verification are not clear [3]. Thus, we need a classifier to help effectively determine the colour clusters on the tongue, so as to make a correct and accurate diagnosis. In order to help daily clinical practice, the implementation of machine learning algorithm was introduced to develop an automatic digital tongue diagnosis system. Support vector machine (SVM) is a supervised machine learning technology. Due to the efficiency and accuracy of this algorithm, it has been widely used in many embedded detection systems, especially in image classification systems [4].

Precision and efficiency, however, is accompanied by a costly and timeconsuming computation cost, this increases the demand for hardware acceleration. Although the software implementation of SVM has high precision, it cannot effectively meet the constraints of real-time embedded system. In such, dedicated hardware must be implemented to meet resource utilization and computing time limitations. The tongue colour diagnosis method of support vector machine (SVM) is studied to characterize and identify the features of modules that affect the system performance, especially in term of computational time. Subsequently, a hardware accelerator will be designed using hardware description language (HDL) to improve performance. The existing and hybrid implementation of the systems are then compared in terms of performance, accuracy and required resources.

1.2 Problem Statement

While software implementation of SVM gives high accuracy, they cannot efficiently meet real time embedded systems' constraints, resource utilization and computation time [4]. This usually happens when the classification problem is huge and the number of support vectors for the solution increases, thus resulting it to be computationally demanding [5].

Many solutions have been proposed to overcome this aspect in software implementation of SVM. One of the solutions is using parallel processing. Indeed, systems with parallel processing processors can be very useful in accelerating such computations, but then the computation then becomes very high in cost [6].

Another problem faced when implementing SVM in software is that CPU implementation will be restricted by the instruction set and the architecture of the system. The CPU can sometimes lack the necessary functional logics to efficiently complete the task [7]. Lastly, running in software tools also mean that additional memory has to be allocated to store the programme code.

With all the problems stated, in order to save resource utilization while maintaining the accuracy of the SVM classifier, CPU implementations can be omitted. Platforms like GPU and FPGA acceleration can be studied to find a method of implementation that fits the requirements of a given application.

1.3 Objectives of Study

This section discusses the objectives set for this project. The objectives are listed below.

- i. To study and identify the compute intensive segments of the baseline Support Vector Machine (SVM) implementation during the classification phase.
- To explore hardware optimization techniques and design hardware accelerator for the compute intensive segment of the classification algorithm using Hardware Description Language (HDL).
- To perform comparative analysis between the baseline and the hardware implementation of the SVM classification phase in terms of performance and accuracy achieved.

1.4 Scope of Study

This section discusses the scope of study set throughout the project. The scope of study are:

- i. Implementation of the existing baseline binary linear SVM training and classification phase for tongue body colour in MATLAB. Red and pink tongue body colour are grouped as the same class, while deep red the other.
- ii. Identify the compute intensive segment of the linear SVM classification algorithm and explore different hardware optimization techniques using Vivado HLS.
- iii. Compare the results obtained from different optimization techniques in terms of maximum achievable frequency, latency in clock cycles, total execution time and hardware cost.
- Develop the hardware accelerator for SVM classification phase and analyse the performance in terms of execution time, total latency in terms of clock cycles and accuracy between the hardware and software implementation.

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