

**HARDWARE-BASED GENETIC ALGORITHM IMPLEMENTATION
IN FIELD PROGRAMMABLE GATE ARRAY**

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HARDWARE-BASED GENETIC ALGORITHM IMPLEMENTATION
IN FIELD PROGRAMMABLE GATE ARRAYS

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A thesis submitted in fulfilment of the
requirements for the award of the degree of
Master of Engineering (Electrical Engineering)

Faculty of Electrical Engineering
Universiti Teknologi Malaysia

MARCH 2012

Dedicated, in thankful appreciation for support, and encouragement to my beloved
family.

ACKNOWLEDGEMENTS

First and foremost, I would like to express my gratitude to my research supervisor, Professor Dr. Haji Mohamed Khalil bin Haji Mohd Hani for his support and patient guidance throughout this research. His constant encouragement, critics and guidance is the key of bringing this research to a fruitful completion.

Besides, I also would like to take this opportunity to thank Dr. Muhammad Nadzir bin Marsono, Dr. Hau Yuan Wen, Ms. Rabia Bakhteri, Vishnu P. Nambiar, Eng Pei Chee, Mohamed Annuar, Liew Teik Yee, and Moganesh who provide me moral support, advices and guidance directly or indirectly in my research.

On a personal note, I thank my family and friends for their seamless caring, encouragement and moral support that enables me to complete this journey.

ABSTRACT

Genetic Algorithm (GA) is inspired by natural selection and evolution in a computer program. It has been shown to be effective in solving search and optimization problems. However, research has shown that software implementations of GA in complex problems usually lead to unacceptable optimization delays. Hence, hardware-based GA solutions are needed, especially in systems that require real-time performance. However, full hardware implementation of GA eliminates its flexibility to be reused in other applications. This is because some of the GA operations are highly problem-dependent. Consequently, this thesis proposes a hardware-based GA (HGA) that provides configurability, scalability and flexibility. The proposed reconfigurable HGA is implemented on Altera Stratix II EP2S60 FPGA prototyping board with a clock frequency running at 50 MHz. Hardware-software co-design technique is applied. The system partitioning is done based on the following aspects: (a) system constraints (b) compute-intensive operations (c) sequential operations (d) bottlenecks during system bus access (e) logic cost, and (f) ability to reconfigure. In this work, the HGA is deployed in a number of test case studies, which include optimization of a simple fitness function, a complex Michalewicz's function, and a real-world finger-vein image processing application. The real-world problem is to apply the GA to optimize the tuning of parameters in a finger-vein image processing biometric subsystem. Experimental results show that the proposed HGA achieves a good degree of configurability and flexibility in handling a variety of problems. The HGA is about three times faster compared to its software equivalent. The equal error rate of the finger-vein biometric system is improved from 1.004% to 0.101%. This shows that the proposed design is capable to optimize the tuning of the parameter set in this image processing application.

ABSTRAK

Algoritma Genetik (GA) berinspirasikan pemilih alam dan evolusi di dalam program komputer. Ianya telah terbukti berkesan dalam menyelesaikan masalah carian dan pengoptimuman. Walaubagaimanapun, kajian telah menunjukkan bahawa pelaksanaan perisian GA dalam masalah yang kompleks biasanya menyebabkan kelewatan dalam proses pengoptimuman yang tidak boleh diterima. Oleh kerana itu, penyelesaian GA yang berasaskan perkakasan diperlukan, terutamanya dalam sistem yang memerlukan prestasi masa nyata. Namun, pelaksanaan penuh GA dalam perkakasan, menyisihkan GA daripada digunakan semula dalam aplikasi lain. Hal ini kerana beberapa operasi dalam GA sangat bergantung kepada masalah yang disasarkan. Oleh itu, tesis ini mencadangkan satu reka bentuk perkakasan GA (HGA) yang menyediakan kemudahan untuk dikonfigurasikan, skalabilitas dan fleksibiliti. HGA yang dicadangkan dilaksanakan pada papan prototaip Altera FPGA II Stratix EP2S60 dengan frekuensi jam yang beroperasi pada 50 MHz. Perkakasan-perisian co-desain teknik diterapkan. Sistem dipartisikan berdasarkan kepada aspek-aspek berikut: (a)kekangan sistem (b) operasi mengira-intensif (c) operasi sekuensial (d) hambatan semasa mengakses bas sistem (e) kos untuk logik digunakan, dan (f) kemampuan untuk mengubah semula konfigurasi. Dalam tesis ini, HGA digunakan dalam beberapa kajian kes, ianya termasuk pengoptimasian fungsi kemampuan yang mudah, fungsi kompleks Michalewicz, dan satu aplikasi dunia nyata. Aplikasi dunia nyata ini adalah penerapan GA untuk mengoptimumkan penalaan parameter untuk pemprosesan gambar vaskular jari dalam subsistem biometrik. Keputusan kajian menunjukkan bahawa HGA yang dicadangkan mencapai tahap yang baik dalam menyediakan kemudahan untuk dikonfigurasikan dan fleksibiliti dalam menangani pelbagai masalah. HGA menghitung sekitar tiga kali ganda lebih cepat berbanding dengan perlaksanaan GA dalam perisian. Nilai *Equal Error Rate* untuk sistem biometrik vaskular jari dapat ditingkatkan dari nilai 1.004% kepada nilai 0.101%. Hal ini menunjukkan bahawa reka bentuk yang dicadangkan adalah cekap dalam mengoptimasikan penalaan parameter yang ditetapkan dalam aplikasi pemprosesan gambar.

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LIST OF ABBREVIATIONS

| | | |
|---------|---|---|
| ASM | - | Algorithmic state machine |
| bmp | - | bitmap |
| CAD | - | Computer-aided design |
| CST | - | Control signal table |
| CPU | - | Central processing unit |
| DFG | - | Data flow graph |
| EER | - | Equal error rate |
| FAR | - | False accept rate |
| FRR | - | False reject rate |
| FPGA | - | Field Programmable Gate Arrays (FPGA) |
| GA | - | Genetic algorithm |
| GCC | - | GNU Compiler Collection |
| GENOCOP | - | Genetic algorithm for numerical optimization for constrained problems |
| GPM | - | Genotype and phenotype mapping |
| HDL | - | Hardware description language |
| HGA | - | Hardware-based GA |
| HW | - | Hardware |
| IDE | - | Integrated development environment |
| MT19937 | - | Mersenne Twisted |
| OS | - | Operating system |
| PC | - | Personal computer |
| PDF | - | Probability Density Function |
| RISC | - | Reduced instruction set computing |

| | | |
|-------|---|--|
| RNG | - | Random number generator |
| ROI | - | Region of interest |
| RTL | - | Register transfer level |
| SDL | - | Simple direct media layer |
| SDRAM | - | Synchronous dynamic random access memory |
| SGA | - | Software-based GA |
| SRAM | - | Static random-access memory |
| SOPC | - | System-on-a-Programmable-Chip |
| SW | - | Software |
| USB | - | Universal serial bus |

LIST OF SYMBOLS

| | | |
|-------|---|-----------------------|
| C_r | - | Crossover rate |
| g | - | Number of generations |
| H_T | - | High threshold |
| L_T | - | Low threshold |
| MHz | - | Mega Hertz |
| M_r | - | Mutation rate |
| ms | - | milliseconds |
| ns | - | nanoseconds |
| P_c | - | Crossover probability |
| ps | - | Population size |
| P_m | - | Mutation probability |

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CHAPTER 1

INTRODUCTION

1.1 Background

Genetic Algorithm (GA) was first introduced by Holland in 1975, GA is based on the idea of evolutionary computing which was introduced by Ingo Rechenberg in 1960 [1]. The GA methodology is about applying natural selection and genetic evolution to solve optimization and searching problem [2]. GA is a well known and a powerful tool for solving search and optimization problems [3]. Besides, GA has been theoretically and empirically proven to provide robust search in complex spaces [1].

GA obeys Darwin's evolutionary theory, "survival of the fittest" where the fitter individuals survive and the weaker individuals die [2]. GA imitates genetic evolution in the form of a computer program. In GA, a chromosome represents a possible solution, and the chromosome is subdivided into genes where genes represent parameters or characters of a problem's solution. Hence in a computer program, the chromosome is in the form of a binary string that have single or multiple genes where each gene has its own position in chromosome.

There are many other algorithms that are conventionally used to solve optimization and search problems such as random search, hill climbing and simulated annealing [4]. GA is different from the conventional problem-solving methods in very fundamental ways [2]:

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