

# Iterative Data Detection and Channel Estimation for Single-Parity Check-Product Coded MIMO Wireless Communications System

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**Abstract:** In iterative data-detection and channel-estimation algorithms, the channel estimator and the data detector recursively exchange the information order to improve the system performance. In this paper, maximum a posteriori based iterative data detection and pilot symbol assisted channel estimation of the single parity check product code for multiple antenna wireless communication is studied. Results show the algorithm can converge at a few iteration numbers and improve error probability performance of the system.

*Index Terms:* multiple antennas, single-parity check codes, product code, iterative decoding, pilot symbol assisted channel estimation.

## I. INTRODUCTION

The joint iterative data-detection and channel estimation appears to be a suitable means to achieve excellent performance in wireless communication system, where the length of the training sequence or pilot symbols is to be kept as small as possible to maintain the data throughput is still high. Basically, iterative schemes (which are usually denoted through the adjective “turbo”), the channel estimator and the data detector recursively exchange information in order to improve the system performance.

The explosive growth of wireless communication services, such as mobile computing and wireless Internet, as well as the deployment of wireless local area network (WLAN), has resulted in an in bandwidth-efficient high-data-rate transmission system. Recent results from information theory have shown that capacity of a multiple antennas wireless communication system operating in a rich scattering environment grows with a law approximately linear in minimum between the number of transmit and receive antennas [1]. Likewise, high-performance space-time diversity systems have been recently introduced, which permit the system to achieve huge performance gains with respect to single-antenna communication system (see. e.g [2], [3], and [4]).

Some layered space-time architectures have been proposed in order to exploit the benefit of the multiple antenna system promise in theory and their potential gains over single antenna systems. Among these, the most popular one has been termed Bell Labs Layered Space-Time Architecture (BLAST) [5]. This system has attracted much attention in the last ten years

and several papers have appeared in the open literature, presenting theoretical finding and/or performance results for BLAST-like system.

In our previous work [6], the full-rate space-time diversity scheme have been proposed. Compare to the system using space-time block codes [3], the proposed scheme has advantage that guarantee the transmission rate will always equal to 1 symbol/Hz/s. This paper extends these previous results by increasing the bit rate and develops the channel estimation. One alternative in increasing bit rate uses high-rate code as component code of the product code but it provides low error rate.

The work in this paper uses the single-parity-check code (SPC) as component code, parallel concatenated, of product code (PC). SPC is simple, but weak, algebraic code. The SPC-PC codes are high-rate codes when reasonable block lengths are used. They are simple to encode and decode and have been shown to produce good performance. Maximum a posteriori (MAP) decoded multidimensional SPC-PC were shown to have very good performance in [7].

By taking the output of the SPC-PC encoder into two defined-interleavers and transmitting through multiple transmit antennas, will provide space and time diversity in the system. Pilot-symbol aided channel estimation is used to measure the channel impulse response. Following the iteration of the decoding process, the channel impulse response will be updated by employing the extrinsic information output of the decoder. The convergence of this channel impulse response update will investigate.

## II. SYSTEM MODEL

Figure 1 depicts the discrete time model of proposed system diagram. At the transmitter, the data stream is formatted into a block data with size of  $k_r \times k_c$ , where  $k_r$  is the data length in rows and  $k_l$  is the data length in column. Each row and column of data is encoded using single parity check code (SPC) to be a  $(k_r+1, k_r, 2)$  for row and  $(k_c+1, k_c, 2)$  for column. The encoding process in rows and columns are to be independent thus the check on check parity bit is not generated [7]. Without lost of generality, in this paper it sets  $k_r = k_c = k$ .



$$L_c(\hat{d}) = L_c(\hat{b}_1) + L_c(\hat{b}_2) + L(d) \quad (9)$$

where  $L_c(\hat{d})$  is simplified version of  $L_c(d|\hat{b})$ . The LLR output (soft output) of the decoder is equal to [8]:

$$L(\hat{d}) = L_c(\hat{d}) + L_e(\hat{d}) \quad (10)$$

where  $L_e(\hat{d})$  is extrinsic LLR obtained from row and column decoding. From [6] and [8], the extrinsic LLR is:

$$L_e(\hat{d}_j) = \log \frac{\Lambda(p+1) \prod_{i=1, i \neq j}^k (\exp(L(\hat{d}_i)) + 1) + \Lambda(p-1) \prod_{i=1, i \neq j}^k (\exp(L(\hat{d}_i)) - 1)}{\Lambda(p+1) \prod_{i=1, i \neq j}^k (\exp(L(\hat{d}_i)) + 1) - \Lambda(p-1) \prod_{i=1, i \neq j}^k (\exp(L(\hat{d}_i)) - 1)} \quad (11)$$

where:

$$\begin{aligned} \Lambda(p+1) &= \exp(L_c(p)) + 1 \\ \Lambda(p-1) &= \exp(L_c(p)) - 1 \end{aligned} \quad (12)$$

where  $L_c(p)$  is LLR channel of the parity bit at the same row or column of the  $d_j$ . At the last iteration, the soft output of the decoder is:

$$L(\hat{d}_j) = L_c(\hat{d}_j) + L_{er}(\hat{d}_j) + L_{ec}(\hat{d}_j) \quad (13)$$

The hard decision value of this data bit can be obtained by applying the sign function to (13).

#### IV. CHANNEL ESTIMATION AND PILOT SEQUENCE DESIGN

Equation (3) can be represented in matrix form as follows:

$$\mathbf{R} = \mathbf{H}\mathbf{X} + \mathbf{W} \quad (14)$$

This relation gives one possible way to measure the channel response. The transmitted block  $\mathbf{X}$  consists of a few number of pilot symbol,  $x_p$ , that the receiver knows the pilot value and its position within the block. A particular pilot symbol will help to measure the channel impulse response and the pilot positions will define the time of those response occurrences. The channel impulse responses will be obtained by inverting the pilot symbol matrix,  $\mathbf{X}_p$ , that the elements of this matrix are pilot symbol from antenna one and two.

$$\mathbf{X}_{p2} = \begin{bmatrix} p_{11} & p_{12} \\ p_{21} & p_{22} \end{bmatrix} \quad (15)$$

This matrix define the minimum number of pilot symbol that should be inserted into the transmitted data block, thus the channel impulse response measurement can be performed.

In order simplified computation of the channel impulse response, this paper sets the pilot symbol are orthogonal in time. In particular, the simplest pilot symbol matrixes are:

$$\begin{bmatrix} p_{11} & 0 \\ 0 & p_{22} \end{bmatrix} \quad \text{or} \quad \begin{bmatrix} 0 & p_{12} \\ p_{21} & 0 \end{bmatrix}$$

Once the channel impulse responses at particular time are achieved, the channel response at a whole transmitted block can be obtained by using interpolation, where the number of pilot symbol define the accuracy of the generated samples.

$$\mathbf{H} = \mathbf{X}^{-1}(\mathbf{R} - \mathbf{W}) \quad (16)$$

As described at previous section, the iterative data detection results the extrinsic information of data that will update the received bits value ( $L_c + L_e$ ). Soft-mapping these bit sequence to the constellation elements of  $S$  (soft-values). By using equation in (5), the new channel impulse response can be calculated. Thus the channel impulse responses are also updated iteratively together with the data detection process.

#### V. SIMULATION RESULT AND DISCUSSION

Simulation results are now presented for the proposed scheme. It begins with comparing the error rate performance of the proposed system with single antenna system. The PC uses SPC with  $k = 8$  and the flat Rayleigh fading channel is assumed. Figure 2 is shown that the proposed system has 2 dB power advantageous than single antenna system to achieve bit error rate (BER) at  $2 \times 10^{-5}$ . Each decoding iteration gave improvement on BER. A significant improvement is obtained after iteration number 3. After that the iteration is not improve the BER performance significantly or no improvement. It has been investigated in [9], that the effective iteration number of 2 dimensional SPC-PC is 3. The curve shows at  $E_b/N_0$  6 dB, the second iteration gives  $1 \times 10^{-5}$  error probability less than the second iteration, while less error probability for  $6 \times 10^{-5}$  gives by the third iteration, and  $1 \times 10^{-5}$  gives by the forth iteration.

Figure 3 shows error rate performance of the proposed system with different number of pilot symbol within one block of data information. The channel is a Rayleigh distributed flat fading and is assumed to be invariant during one symbol period. Number of pilot symbols compare to the number of symbols in one data block is presented in percentage. For the number of pilot symbols equal to data symbols (50%), gives BER  $2 \times 10^{-5}$  at  $E_b/N_0$  6 dB, while perfect channel estimation gives  $3 \times 10^{-6}$ . This error performance decreases significantly as decreasing the number of pilot symbols. If half number of pilot symbols is employed, the error performance will decrease to  $2 \times 10^{-5}$ . Furthermore, the error rate performance will decrease to  $1 \times 10^{-4}$  if the number of pilot symbol is a quarter of transmitted block length, and  $2 \times 10^{-3}$  if the number of pilot is one over eight of the length of transmitted block. These results confirmed that higher number of pilot symbols will give higher error rate performance in the cost of lower rate transmission.



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