# STUDY OF INTERFACIAL REACTION DURING REFLOW SOLDERING OF Sn Sn-Ag-Cu LEAD-FREE SOLDERS ON BARE COPPPER AND IMMERSION SILVER SURFACE FINISHES

NURFAZLIN BINTI ABU HASSAN

UNIVERSITI TEKNOLOGI MALAYSIA

# STUDY OF INTERFACIAL REACTION DURING REFLOW SOLDERING OF Sn-Ag-Cu LEAD-FREE SOLDERS ON BARE COPPPER AND IMMERSION SILVER SURFACE FINISHES

## NURFAZLIN BINTI ABU HASSAN

A project report submitted in partial fulfilment of the requirements for the award of the degree of Master of Engineering (Mechanical-Materials)

> Faculty of Mechanical Engineering Universiti Teknologi Malaysia

> > APRIL 2009

#### ABSTRACT

Due to increasing environmental and health concerns related to the toxicity of traditional tin-lead solders, lead-free solders appear as promising replacement for the eutectic solder alloy in flip chip technology. Then a surface finish is designed to protect the copper surface against oxidation as well as provide a diffusion barrier against rapid diffusion between the liquid tin in the solder alloy and copper. In this research, an investigation has been carried out to investigate the effect of multiple reflow and solder bump size during soldering between three types of lead-free solders which are Sn-3.0Ag-0.5Cu, Sn-3.8Ag-0.7Cu and Sn-4.0Ag-0.5Cu on Copper and Immersion Silver (ImAg) surface finishes. Two different solder ball sizes were used; 200 µm and 700µm, and the solder joints were subjected to three reflows at the same temperature. The IMC's formed were characterized in terms of thickness, morphology and composition. From the research findings, it was observed that solder bump size, which also relates to solder volume, has a significant effect on the formation of intermetallics thickness in the solder joint. The mean thickness of the intermetallics for smaller solder balls was found to be thicker than the bigger solder balls. ImAg produced thinner IMC's compared to copper surface finish. However, these IMCs grew thicker and changed their morphologies when exposed to multiple reflow, but the compositions were more or less the same. For the Cu surface finish, Cu<sub>6</sub>Sn<sub>5</sub> intermetallic compounds with scallop morphology are formed at the solder/ surface finish interface.

#### ABSTRAK

Berkaitan peningkatan isu-isu alam sekitar dan masalah kesihatan berhubung pada toksik semulajadi dari pateri plumbum, aloi pateri tanpa plumbum, iaitu Sn-4Ag-0.5Cu telah wujud sebagai salah satu calon terunggul untuk menggantikan aloi eutektik Sn-Pb dalam teknologi penyambungan flip chip. Kemudian kemasan permukaan diperlukan untuk melindungi permukaan kuprum daripada pengoksidaan dan ia juga membentuk lapisan penghalang dari resapan mengejut antara atom-atom timah dalam logam pemateri dengan kuprum. Dalam projek ini, kajian dijalankan untuk memeriksa kesan reflow berganda dan saiz bebola pateri semasa pematerian antara tiga jenis aloi pateri tanpa plumbum; Sn-3.0Ag-0.5Cu, Sn-3.8Ag-0.7Cu dan Sn-4Ag-0.5Cu dengan kemasan permukaan Cu dan rendaman silve (ImAg).Dua jenis saiz bebola pateri digunakan iaitu 200 µm dan700µm, dan sambungan pematerian ini dilakukan reflow sebanyak tiga kali pada suhu yang sama. Sebatian antara logam yang terbentuk dikaji dari segi ketebalan, morfologi dan komposisi. Daripada kajian, didapati saiz babola pateri banyak mempengaruhi pembentukan IMC. Ketebalan purata IMC untuk bebola pateri bersaiz kecil didapati lebih tebal berbanding bebola pateri bersaiz besar. Manakala kemasan permukaan memperlahankan kadar tindak balas antara logam pateri dan substrat. ImAg menghasilkan IMC yang paling nipis berbanding Cu. Walaubagaimanapun, IMC ini menjadi semakin tebal dan berubah dari segi morfologi apabila didedahkan kepada reflow berganda, tetapi komposisinya adalah lebih kurang sama. Untuk Cu, sebatian antara logam yang terbentuk adalah Cu<sub>6</sub>Sn<sub>5</sub> dengan bentuk siku keluang (scallop) di lapisan antara logam pemateri dengan kemasan permukaan.

## TABLE OF CONTENTS

CHAPTER	TITLE	PAGE
	DECLARATION	ii
	DEDICATION	iii
	ACKNOWLEDGEMENTS	iv
	ABSTRACT	V
	ABSTRAK	vi
	TABLE OF CONTENTS	vii
	LIST OF TABLES	xii
	LIST OF FIGURES	xiii

INTRODUCTION		1	
1.1	Introduction	1	
1.2	Objectives of the Research	2	
1.3	Scopes of the Research	3	
1.4	Structure of Thesis	3	

1

LITERATURE REVIEW - ELECTRONIC PACKAGING 4

viii

2.1	Introd	uction	4
	2.1.1	Functions of Electronic Packaging	5
	2.1.2	Hierarchy of Electronic Packaging	7
2.2	Chip I	Level Interconnection	9
	2.2.1	Wire Bonding	9
		2.2.1.1 Wire Bonding Techniques	10
		2.2.1.2 Advantages and Disadvantages of Wire	
		Bonding Interconnection	12
	2.2.2	Tape Automated Bonding	12
		2.2.2.1 Tape Automated Bonding Assembly	14
		2.2.2.2 Advantages and Disadvantages of	
		Tape Automated Bonding	16
	2.2.3	Flip Chip Bonding	17
		2.2.3.1 Solder Bump Structure For Flip Chip	
		Interconnection	18
		2.2.3.2 Solder Bump Flip Chip Process	18
		2.2.3.3 Flip Chip Bonding Methods	21
		2.2.3.4 Advantages and Disadvantages of Flip Chip	23
LITE	RATUI	RE REVIEW - SURFACE FINISH SYSTEMS	26
0.1	T / 1		26

3.1	Introduction		
3.2.	Functions of Surface Finish		
3.3.	Coating Thickness		
3.4.	. Surface Finish System		
	3.4.1 Hot-Air Sol	der Leveling (HASL)	31
	3.4.2 Organic Solo	derability Preservative (OSP) Finish	32
	3.4.3 Immersion S	Silver	34
	3.4.4 Immersion T	Tin	36
	3.4.5 Electroless N	Nickel/ Immersion Gold	38
	3.4.6 Nickel/ Palla	adium/ Gold Finish	40
3.5	Summary of Surface Finish System		
3.6	Chemistry and Process Description of Surface Finish		
	Technologies		45

2

3

LITERATURE REVIEW - SOLDERING 47 4.1 Introduction 47 4.2 Solder Material 48 4.2.1 Lead Based Solders 49 4.2.2 Lead Free Solders 51 4.2.3 Alloy Selection 52 54 4.3 Soldering Techniques 4.3.1 Reflow Soldering 55 4.3.2 Wave Soldering 61 4.3.3 Hand soldering 62 4.4 Flux 63 4.4.1 Functions of Flux 63 4.4.2 Types of Flux 64 4.4.2.1 Rosin/Resin based fluxes 64 4.4.2.1 Water soluble flux 65 4.4.2.3 No clean fluxes 66 4.5 Solderability 66 **INTERMETALLIC COMPOUNDS** 68 5.1 Introduction 68 5.2 Formation of IMC in Solder Joint 70 5.3 Factors That Contribute To The Formation of IMCs 72 5.4 73 IMCs formed in Sn-Ag-Cu Solder Joint RESEARCH METHODOLOGY 75 5

RESE	АКСП	METHODOLOGI	15
6.1	Introduction		
6.2	Substrate Material		
6.3	Substrate Pre Treatment		
6.4	Immersion Silver Plating		
6.5	Reflow	v Soldering	80
	6.5.1	Solder Mask	81
	6.5.2	Flux Application	82
	6.5.3	Solder Ball Placement	83
	6.5.4	Reflow Soldering Process	83

5

6

4

	6.6	Chara	cterization of the IMCs	84
		6.6.1	Characterization of Specimen Cross Section	85
		6.6.2	Characterization of Specimen Top Surface	87
7	RES	ULTS A	ND DISCUSSION	88
	7.1	Introd	luction	88
	7.2	Intern	netallic Compound Determination	88
	7.3	Surfac	ce Morphology of IMC Compound	91
		7.3.1	Intermetallics between Sn-3Ag-0.5Cu (SAC 305	)
			and Surface Finish	91
			7.3.1.1 Multiple Reflow Soldering on Copper	
			Surface Finish	91
			7.3.1.2 Multiple Reflow Soldering on Immersion	l
			Silver Surface Finish	94
		7.3.2	Intermetallics between Sn-3.8Ag-0.7Cu (SAC	3.807)
			and Surface Finish	97
			7.3.2.1 Multiple Reflow Soldering on Bare	Copper
			Surface Finish	97
			7.3.2.2 Multiple Reflow Soldering on Immersion	Silver
			Surface Finish	99
		7.3.3	Intermetallics between Sn-4Ag-0.5Cu (SAC 40	5) and
			Surface Finish	100
			7.3.3.1 Multiple Reflow Soldering on Bare	Copper
			Surface Finish	100
			7.3.3.2 Multiple Reflow Soldering on Immersion	Silver
			Surface Finish	103
	7.4		Thickness of Intermetallic Compound	105
		7.4.1	Effect of Solder Bump Size on Intern	netallic
			Compound Thickness	106
		7.4.2	Effect of Surface Finishes on Intermetallic Con	npound
			Thickness	108
		7.4.3	Effect of Multiple Reflow on Intermetallic Con	npound
			Thickness	111

### **CHAPTER 1**

### **INTRODUCTION**

#### 1.1 Introduction

In the current industrial world, soldering has become indispensable for the interconnection and packaging of virtually all electronic devices and circuits. The fast changing technology and increasing miniaturization of electronic devices place challenge for obtaining reliable and successful component joints. This is another reason which has pressed the researchers to investigate and develop the novel solder materials to meet the requirements and a goal of the modern day solder interconnects. Almost all the potential lead-free solder materials are high Sn-containing alloys, including the most promising Sn-Ag-Cu (SAC) alloy of eutectic composition.

During the soldering process, the metallization layer of the printed circuit board (PCB) or the component pins react with Sn in the solder to form an intermetallic compound (IMC) layer at the metallization/solder interface. Although the formation of the IMC layer is desirable for good wetting and necessary bonding, an excessively thick layer is detrimental to the health and reliability of solder joints. The problem arises due

to the brittle nature of the IMC which makes it prone to mechanical failure even at low loads. A thicker IMC layer would also increase the heterogeneity in the physical properties of material across the joint. Therefore, the growth rate of the IMC layer and subsequent dissolution of the metallization (substrate) must be under control during soldering.

Thus, reliability issues are usually closely related to the solder joints intermetallic compounds. During reflow soldering, due to the high temperature conditions, a reaction will usually occur between the solders and the surface finish layers which in turn forms layers of intermetallic compounds at the interface of the solder joints. These intermetallic compounds are required for the formation of solder joint. However, excessive thickness of IMCs will embrittle the solder joint due to its hard and brittle properties in nature. Therefore, study and characterization on the IMCs formed are essential in order to achieve a good and reliable solder joint.

#### **1.2** Objectives of the research

The main objectives in this research are as follows:-

The intent of this study is first to examine the effect of multiple reflow on interfacial reactions (IMC) during soldering between Sn-Ag-Cu Solders and Cu and ImAg finishes. The secondary objective is to examine the effect of Cu and Sn contents in SAC solder on the interfacial reactions on Cu and ImAg surface finishes and lastly to investigate the effect of solder bump size (solder volume) on the formation and growth of the intermetallics between the various surface finish and different lead-free solders.

#### **1.3** Scope of Research

Solder joints in the present work are produced by doing multiple reflow until three times numbers of reflow soldering between immersion silver and bare coppers as surface finishes with three types of lead free solders (SAC305, SAC3.807 and SAC405) which having diameter of around 200µm and 700µm. Characterization is done on the IMCs to determine their compositions and morphologies, and the IMC thickness for all specimens is also measured.

#### **1.4** Structure of Thesis

This thesis comprises eight chapters. The first chapter is the introduction. The objectives and scopes of this thesis are also clearly stated in this chapter. Chapter two to five are the literature review. Chapter two is a detailed introduction of electronic packaging. Chapter three discusses the different types of surface finish systems. In chapter four soldering basics, solder alloys, fluxes and soldering techniques are reviewed. Towards the end of literature review in this study is chapter five which is a, intermetallic compound formation at the interface of solder joints. In chapter six, a detailed experimental methodology is presented to give a better idea of how samples and specimens are prepared based on the most recommended procedures. Chapter seven contains the results and discussion obtained from the experimental work. The final chapter, chapter eight, includes a set of conclusions drawn based on the results and discussion conducted and also recommendation for future work.

#### REFERENCES

- 1. Chiou, B.-S. (2006). *Microelectronics Packaging*, CRC Press.
- 2. Seraphim, D.P., Lasky, R.C., and Li, C.Y (1989). *Principles of Electronics Packaging, Introduction*. New York, McGraw-Hill.
- Tummala, R. (2001). "Fundamentals of Microsystems Packaging". New York, Mc-Graw Hill.
- 4. Harper, C. A. (2005). *Electronic Packaging and Interconnection Handbook, Fourth Edition*. New York, McGraw Hill.
- 5. Greg, W. J. (2007). Integrated Circuit Packaging, Assembly and Interconnections, First Edition, Springer.
- D.S. Liu a, Y.C. Chao a, C.H. Lin a, G.S. Shen b, H.S. Liu b (2003). Numerical study on the bonding tool position, tip profile and planarity angle influences on TAB/ILB interconnection reliability. Microelectronics Reliability 43: 935–943.
- G. E. Servais & S.D. Brandenburg (1991). Wire Bonding A Closer Look. ISTFA'91: The 17th International Symposium for Testing & Failure Analysis, California, USA.
- 8. Harry K. Charles, J. The Wirebonded Interconnect: A Mainstay for Electronics.
- Lau, J. H. (1994). Chip on Board Technologies for Multichip Modules. Van Nostrand Reinhold.
- Haksoo Han, Hyunsoo Chung, Yung-Il Joe, Seongsu Park, Gwangchong Joo, Nam Hwang, and Minkyu Song (1998). The Application of Flip-Chip Bonding Interconnection Technique on the Module Assembly of 10 Gbps Laser Diode. on the Module Assem 27(No 8).
- Gilleo, K. (2002). Introduction to Electronic Packaging. In: Gilleo, K. ed. Area Array Packaging Handbook-Manufacturing and Assembly. New York: McGraw-Hill. 3-21.
- Parquet, D. T. a. S. A. S. *Electroless Ni/Au as an HASL alternative*, Electronic Packaging and Production.

- 14. O'Connell, J. "Senior consultant study and recommendations into using lead free printed circuit board finishes at manufacturing in circuit test stage".
- Johal, H. R. a. K. 2007. *Lead Free Board Surface Finishes*, Lead-Free Soldering, Springer US: 221-269.
- Vianco, P.T. 1998. An Overview Of Surface Finishes And Their Role In Printed Circuit Board Solderability And Solder Joint Performance vol.25(1) p.6-24. USA: MCB University Press
- 17. Cullen, D. P., Millad. G. (2004). Implementation of Immersion Silver PCB SurfaceFinish in Compliance With Underwriters Laboratories
- Jon O'Connell (B.ENG), (AMIEE), Senior Consultant Study and recommendations into using lead free printed circuit board finishes at manufacturing in circuit test stage
- Cemco-FSL (2005) The Newest Surface Finish Alternative: "LEAD-FREE HASL - Its Development and Advantages".
- 20. Milad, G. 2007. *Surface Finishing For Lead-Free*. Southington City: Uyemura International Corporation
- 21. Harper, Charles A. and Sampson, Ronald M. 1994, *Electronic Materials & Processes Handbook*, Second Edition, McGraw-Hill, Inc.
- Merix Corporation (2006) Comparison of Electroless Ni/Immersion Au vs. Electrolytic Ni/Au. (www.merix.com)
- 22. Lamprecht, S et al. (2005), *Implementing Green PCB Production Processes*. The Board Authority, Vol. 6, No. 1.
- 23. Parquet, Dan T. and Spence A. Sedacca. 1996. *Electroless Ni/Au as an HASL alternative*. Electronic Packaging and Production. March.
- 24. Suganuma, K. 2004. "*Lead-Free Soldering In Electronics*". New York: Marcel Dekker, Inc
- 25. Suganuma, K 2001. Advanced In Lead-Free Electronics Soldering. Journal of Solid State& Materials Science, volume (5): 55-64
- Lim, C.K. 2006. Evaluation Of Lead-Free Solder: A study Of Intermetallics. Universiti Technology Malaysia: B.Sc. Thesis

- 27. Elenius, P. 1997. Flex on Cap-Solder Paste Bumping and Technology Conference. pp(248)
- Harrison, M. R., Vincent, J. H. and Steen, H. A. H. (2001). Lead-free Reflow Soldering for Electronics Assembly. *Soldering & Surface Mount Technology*. 13 (3): 21-38.
- 29. Tai, S.F. 2003. *Materials Interaction during Metallic Interconnection Process*. Universiti Teknologi Malaysia: M. Eng. Thesis
- Blackwell, G. R. (2000). Surface Mount Technology. In: Blackwell, G. R. ed. The Electronic Packaging Handbook. Florida: CRC Press. 2.1-2.44.
- 31. Strauss, R. (1994). Surface Mount Technology. Oxford: Butterworth-Heinemann.
- 32. Blaine Partee, 2004. *Interfaces in Lead-Free Soldering* (http://www.empf.org/empfasis/june04/inter.htm)
- Hwang, J. S. (1992). Solder Paste in Electronics Packaging. New York: Van Nostrand Reinhold.
- Kang, S.k, Lauro, P.A, Shih, D.Y, Henderson, D.W & Puttlitz, K.J. 2005. *Microstructure and Mechanical Properties of Lead-Free Solders and Solder Joints Used In Microelectronic Applications*. IBM J. Research and Development; vol.(49) no. 4/5.
- 35. J.H. Lau, Solder Joint Reliability-Theory and Application, Van Nostrand Reinhold, New York, 1991.
- 36. Sharif, A., Chan, Y. C. Islam. (2003). Effect of Volume in Interfacial Reaction Between Eutectic Sn-Pb solder and Cu Metallization in Microelectronic Packaging, Department of Electronic Engineering, City University of Hong Kong
- 37. Faizan, M. (2007). *Experimental Study And Modelling Of Metal Dissolution and Intermetallic Compound Growth During Soldering*, University of Akron. Doctor of Philosoph,180.
- 38. Aisha. S.R.I. 2006. Effect Of Multiple Reflow And Solder Bump Size On Intermetallic Compound Formation Between Sn-4Ag-0.5Cu and ENIG surface finish. UTM: B.Sc Thesis
- 39. Solberg, V. (2004) "Selecting the Right Surface Finish" (http://smt.pennnet.com)